

CARMEQ

Using Behavior Models for the Specification of Software based automotive Systems – Challenges and practical Experiences

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Behavior Models for Software Specification

Agenda

- Challenges
- Model supported Specification
- > Variant Management

Development of Electronic Control Units (ECU) Challenges

- Increased requirement complexity in less time to market
 - Reduce failure rate and correction cycles during development
 - > Provide precise requirement specification to ECU supplier
 - Description of either complex state based logic or control strategies, depending on the application
- Management of increasing functional variability
 - > Product line / platform strategy
 - > Hundreds of variation points in single ECUs software
 - Explicit variant modelling by using feature models











Behavior Models for Software Specification

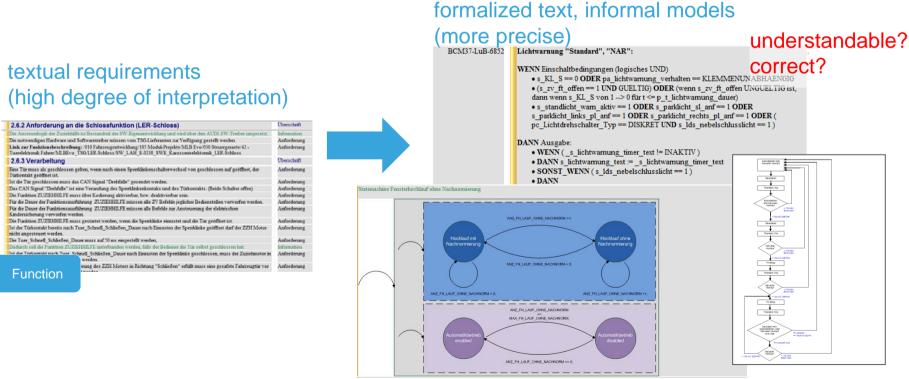
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- > Model supported Specification
- > Variant Management

ECU Specification

Use of Models

- > Requirements Specifications have to be precise and consistent
 - In order to get the correct functionality fast
 - In order to get the same functionality from different suppliers
 (especially true for multi supplier strategy)



Use of Behavior Models

Model supported Specification

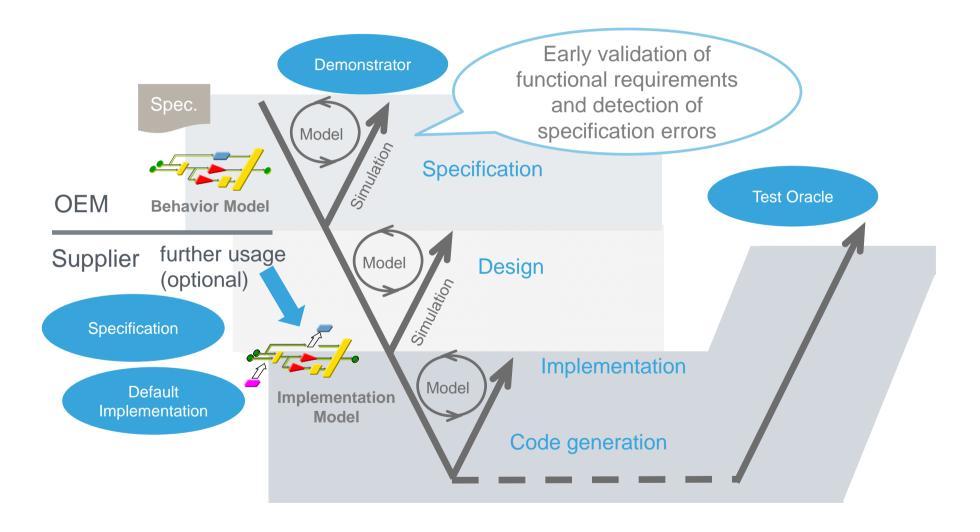
- Motivation
 - Early usage of models with defined semantic
 - > Proof of correctness by simulation
 - Mainly Simulink/Stateflow used

formalized text, informal models BCM37-LuB-6852 Lichtwarnung "Standard", "NAR": WENN Einschaltbedingungen (logisches UND) • s KL S == 0 ODER pa lichtwarnung verhalten == KLEMMENUNABHAENGIG • (s zv ft offen == 1 UND GUELTIG) ODER (wenn s zv ft offen UNGUELTIG ist, dann wenn s KL S von 1 --> 0 für t <= p t lichtwamung dauer) DENORMIERT • s_standlicht_warn_aktiv == 1 ODER s_parklicht_sl_anf == 1 ODER s_parklicht_links_pl_anf == 1 ODER s_parklicht_rechts_pl_anf == 1 ODER (pc Lichtdrehschalter Typ == DISKRET UND s lds nebelschlusslicht == 1) KEINE_ANF_LH_UMGESETZT • WENN (s lichtwarnung timer text != INAKTIV) • DANN s_lichtwarnung_text := _s_lichtwarnung_timer_text entry: rNormLHOeffnungProz = SONST WENN (s lds nebelschlusslicht == 1) /* Langhub-Fkt. aktiv und relative Wegstr oder unterer Block erkannt */ [bVDLanghubAktiv 8... (bStFHUntererBlock || IbOberhalbUHPos _s1_anf == 1) ANF_LH_UMGESETZT rechts_pl_anf == entry: rNormLHOeffnungProz = :

simulation model

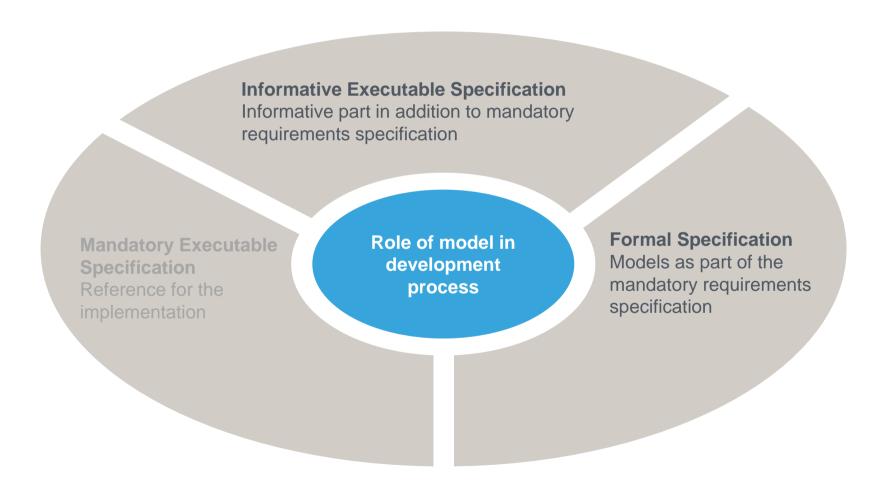
Use of Behavior Models

Process View and Benefit



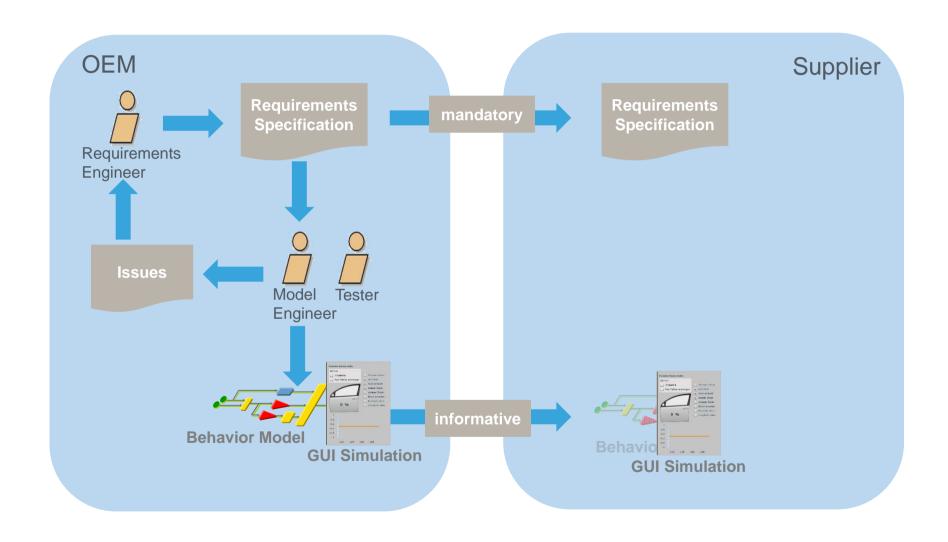
Model supported Specification

Role of models in development process



Role of Models

Informative Executable Specification



Informative Executable Specification

Experiences

advantage

- Improvement in the requirements specification
- Better understanding about functionalities on OEM and supplier side
- Reduction in development time, since less requirements have to be clarified
- > Reduction of costs for CRs

disadvantage

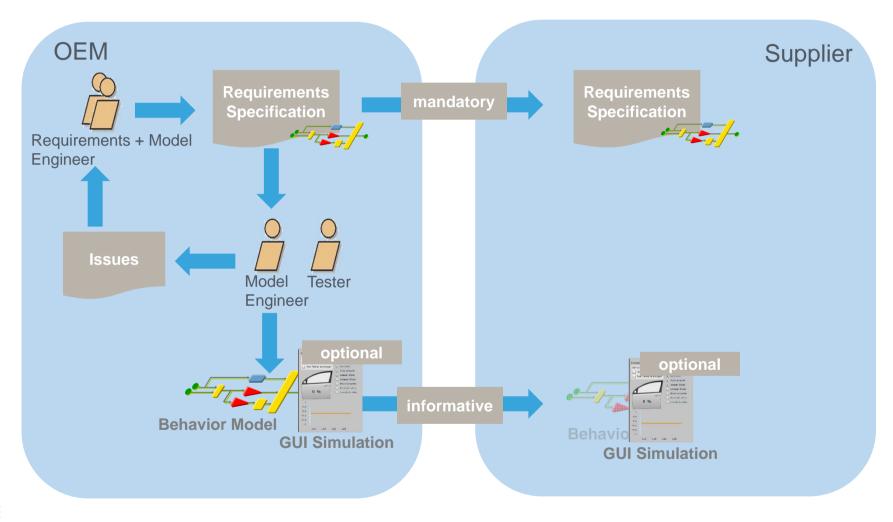
- High effort (and thereby cost) has to be spent in modelling activities
- Requirements are still interpretable, since models do not have direct impact on specification method
- Risk of inconsistency between requirements specification and model

- > The improvement in the requirements specification (and the costs saved by this improvements) are usually compensated by the effort spend for modelling activities
- This strategy is profitable only, when models are further used for another purpose, e.g. rapid prototyping or series software implementation

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Role of Models

Formal Specification

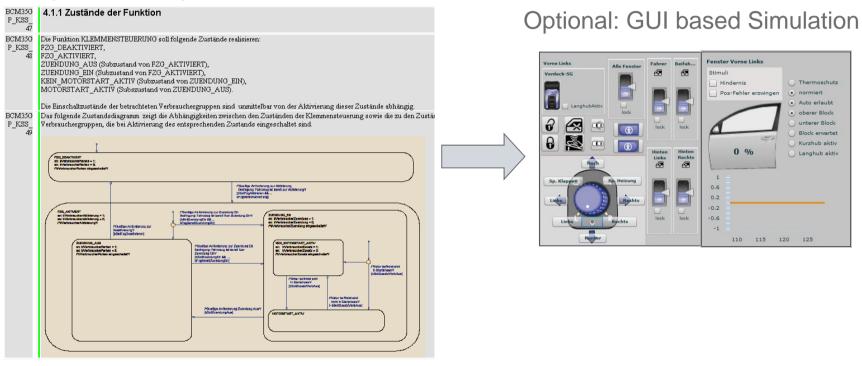


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Formal Specification

Characteristics

Requirements specification



Usually, only a part of the specification is replaced by models

Formal Specification

Experiences

advantage

- Improvement in the requirements specification
- Better understanding about functionalities on OEM and supplier side
- Low degree of interpretation
- Reduction in development time, since less requirements have to be clarified
- > Reduction of costs for CRs

disadvantage

Still initially high effort, but this is scalable, depending on the degree of modelling

- > Improvement in the requirements specification with scalable effort
- Decause only relevant parts of the specification are modeled, effort and cost for modelling activities are lower

Model supported Specification

Conclusions

- Model supported specification promises a variety of advantages
 - Less misinterpretation of requirements
 - Faster development cycles
 - Less communication effort
 - Detter understanding of the underlying functionalities
- In order to obtain the most benefit
 - Models should play a mandatory role
 - Models should be exchanged with suppliers
- Initial high effort is notably justified in case of further usage of models

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Motivation and Objectives

Motivation

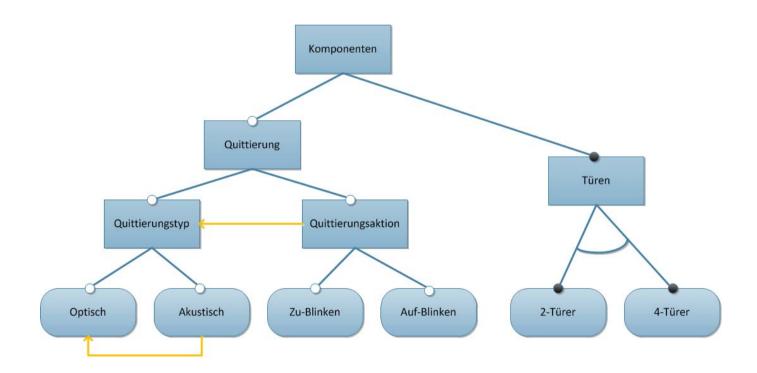
- Software functions have to be deployed for different platforms and vehicles
- > Requirements differ slightly for each platform
- > Set of relevant functions differ for each platform
- Software has to cope with these differences

> Objectives

- Explicit variability management for models and requirements
- Maintaining consistency between requirements and model variants
- Provide a single software model which is able to handle defined variants
- Management of similar software products (software product lines)

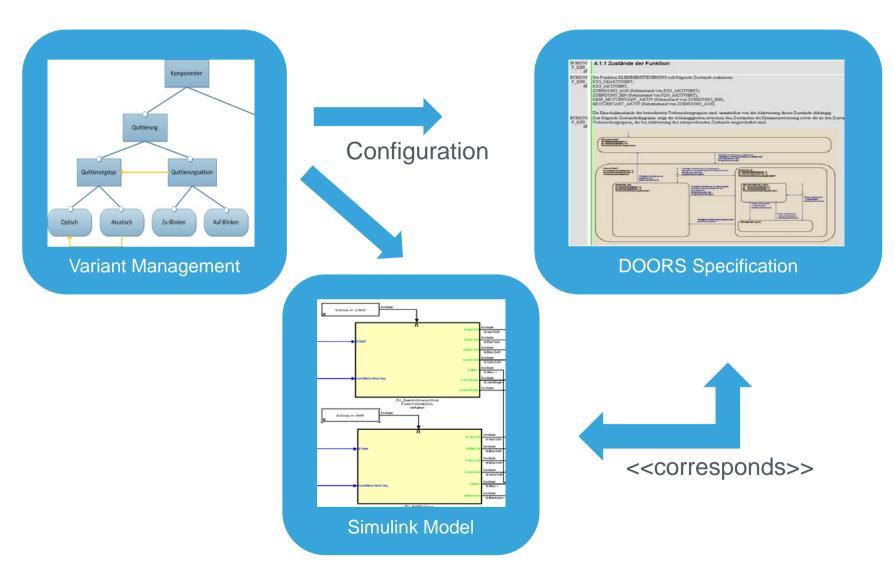
Feature Models

- > Feature models explicitly describe variants in the software (Kang et.al., 1990)
- > To reduce combinatorial multiplicity, relationships between features have to be defined



Overview Variant handling

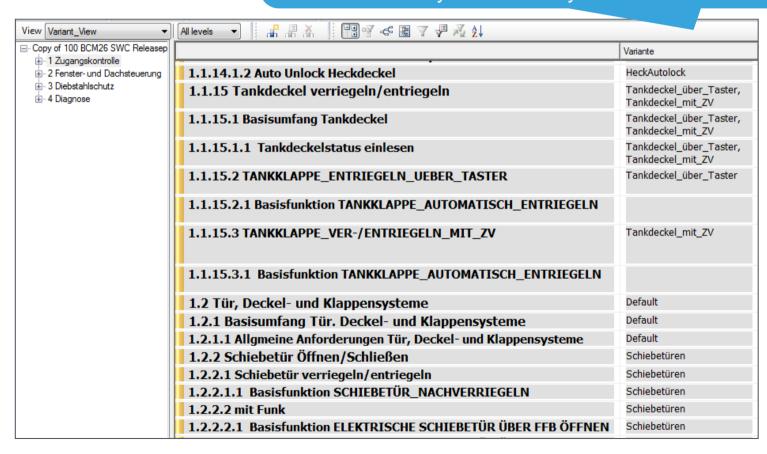
Model Supported Specification



DOORS Specification

Variants are maintained in feature models and are annotated to DOORS requirements.

Feature selections are created by filters generated by the variability tool.

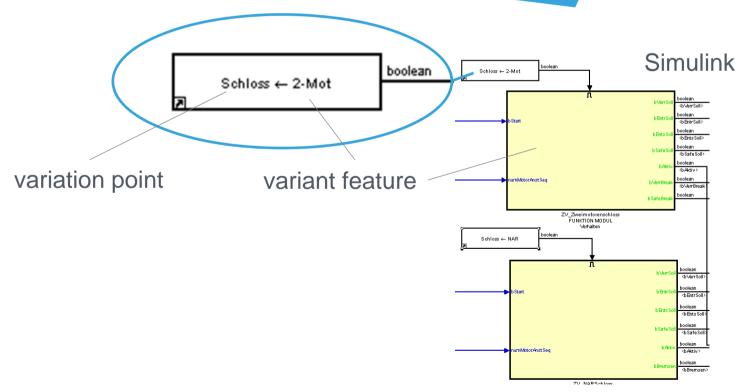


Simulink Model

Variant features are modeled in different subsystems.

Correspondence to feature model is realized by special parameters.

For each variation point in the feature model a separate parameter exists.



Variant Binding Time

Simulink Model

Design Time

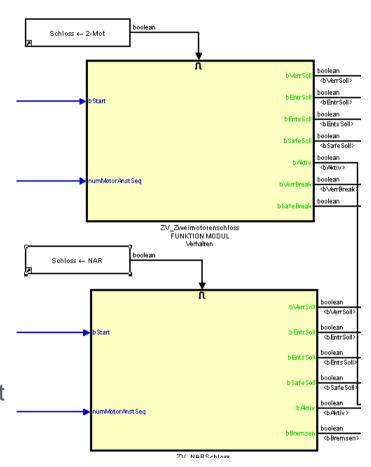
Variants are selected for a specific project,
 e.g. they are fixed for given project

Compile Time

- Variants are determined at compile time
- Deselected variants are removed from production code via specific storage classes

> Run Time

Variants are expressed via parameters that can be changed during run time.

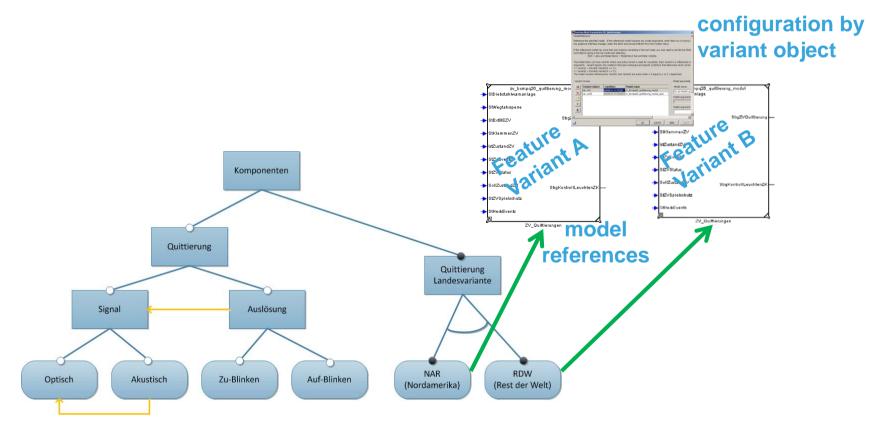


Compile Time and Run Time Variants are modelled as "Enabled" Subsystems

Variant Binding Time

Simulink Design Time Variants

- > Are modelled as model references
- Variants are selected by variant objects
- > Different implementations can be selected for specific projects.



Conclusions

- We use a variant model that coordinates requirements in DOORS as well as the Simulink model
 - Variability is explicitly defined for all stakeholders
 - Dependencies between variation points are defined
 - Less errors in ECU software configuration
- We handle all variants in a single Simulink model hierarchy, which includes constructs for all kinds of binding time variants
- > Further work
 - Improvements in tool integration needed: feature modelling to requirements management and modelling tools
 - Links between variant models of ECU functions and vehicle variant model

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